RAK4270 WisDuo LPWAN Module

Thank you for choosing **RAK4270 WisDuo LPWAN Module** in your awesome IoT Project! For help you get started, we have provided you all the necessary documentation for your product.

- Quick Start Guide
- AT Command Manual
- RUI Customized Development RAK4270 module supports RUI (RAKwireless Unified Interface) Customized Development.
- Low Level Development
- Datasheet
- RAK4270 Module 3D Model ☐
- Reference Design

Product Description

The RAK4270 LoRa Module includes an **STM32L071 MCU** and an **SX1262** LoRa chip, which supports eight spreading factors (SF5 ~ SF12) and signals bandwidth that can be adjusted between 7.8 kHz to 500 kHz. It has Ultra-Low Power Consumption of 2.31 μ A (down to 1.61 μ A @ 2.0 V) in sleep mode, but during the Transmit Mode, it can reach the maximum output power of 22 dBm. As a receiver, it can achieve a sensitivity of -148 dBm.

The module complies with Class A and C of LoRaWAN 1.0.2 specifications, and it also supports LoRa Point-to-Point (P2P) communications. The module is suitable for various applications that require long-range data acquisition and low power consumption, such as smart meters, supply chain and logistics tracking, agricultural sensors, and smart cities.

You can configure the mode and operation of the RAK4270 module using the AT Commands via a UART interface. Also, it offers low-power features that are very suitable for battery-powered applications.

Features

- LoRa module is suitable for applications in Smart City, Smart Agriculture, Smart Industry
- Compact form factor: 15 x 15.5 x 2.5 mm (LxWxH)
- 20 pin stamp pad for PCB SMT board-to-board soldering
- I/O ports: UART/I2C/GPIO/ADC
- · AT commands control interface
- Temperature range: -30 °C to +85 °C
- Supply voltage: 2.0 to 3.6 V
- Supported bands: (EU433, CN470, IN865, EU868, AU915, US915, KR920, and AS923-1/2/3/4)
- LoRa bandwidth range of 7.8 kHz to 500 kHz, SF5 to SF12, BR=0.018~62.5 kb/s
- Ultra-Low Power Consumption of 2.31 μA (down to 1.61 μA @ 2.0V) in sleep mode
- ARM Cortex-M0+ 32-bit RISC core
- · 128 kbytes flash memory with ECC
- · 20 kbytes RAM
- 6 kbytes of data EEPROM with ECC
- · Compliance with TBC