

RAK4260 WisDuo LPWAN Module

Thank you for choosing **RAK4260 WisDuo LPWAN Module** in your awesome IoT Project! 🎉 To help you get started, we have provided you all the necessary documentation for your product.

- [Quick Start Guide](#)
- [Datasheet](#)
- [Microchip LoRaWAN Stack \(MLS\) Demo](#) [↗](#)
- [RAK4260 3D Model](#) [↗](#)
- [Reference Design](#) [↗](#)

Product Description

The **RAK4260 WisDuo LPWAN Module** is based on **Microchip's ATSAMR34J18B**. It is a SiP device integrating a 32-bit ARM Cortex M0+ MCU with a LoRa Transceiver in a 15 mm x 15 mm compact package.

The SAMR chip provides a number of highly configurable peripherals (configurable as I2C/SPI/UART interfaces). There are 12-bit ADC in addition to the aforementioned.

It is a perfect solution for any LoRaWAN end node developer. The integration of the MCU and LoRa Transceiver reduces size and minimizes costs. Having such a compact solution within a single package reduces time to market and allows for rapid development and deployment for a number of scenarios.

RAK4260 is a solution that is cost efficient and flexible that can be deployed in a wide variety of IoT scenarios that require the long range connectivity and great battery life that LoRaWAN provides.

Product Features

- Industry's lowest power LoRa SiP device
- 32-bit ARM Cortex M0+ MCU and LoRa Transceiver
- Small form factor: 15 mm x 15 mm compact package
- 256 KB Flash and 40 KB RAM accommodates application code and stack
- Most cost and size effective solution, eliminating the need for external MCU
- Fully supported 862 to 1020 MHz frequency coverage
- Receive Sensitivity down to -148 dBm
- Maximum Transmit Power up to 20 dBm
- Low RX current of 17 mA (typical)
- LoRa Technology, (G)FSK, (G)MSK

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